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Abstract

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Pulse forming circuit for thyristor **control** contains **capacitor charging** circuit, avalanche transistor in series with load and pulse **transformer**. To provide steep pulse front the primary winding of **transformer** is connected through diode to control potential.

In self-oscillating mode capacitor (2) charges from potential on terminal (4) through resistor (3). When the potential on capacitor (2) reaches switching threshold of avalanche transistor (1), the transistor operates and passes into saturation by positive feedback from **transformer** (5). The sequence generates switching pulse in load (14).

After a fixed interval diode (15) opens and control potential is applied to transistor (1). **Transformer** (5) saturates and transistor (1) cuts off. Diode (10) and resistor (11) prevent parasitic generation of pulse. The sequence is repeated with recharging of capacitor (2).

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